

**CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8)**

Applicant(s): TSUTOMU ISHIKAWA, ET AL

Docket No.

YKI-0032

Serial No.

09/552,085

Filing Date

2/24/2000

Examiner

L. NGUYEN

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Group Art Unit

2816

Invention: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING PADS WITH LESS INPUT  
SIGNAL ATTENUATION

TECHNOLOGY CENTER 2800

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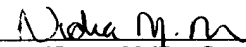
Assistant Commissioner for Patents, Washington, D.C. 20231 on

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